



# Trizeps III

## XScale PXA255 CPU Module SO-DIMM 200

Product Brief February 2005, V1.2

### CPU-Module Trizeps-III

- Intel XScale PXA255 with 200, 300, 400 MHz
- Jtag debug port via flexcon
- Reset generator
- High efficiency switch mode core voltage regulator
- 32 / 64 MByte synchronous burst K3-FLASH, 32bit buswidth
- 32 / 64 / 128 MByte LP-SDRAM
- 4 Serial IF: FFunction, HW-Uart, Bluetooth uart, IrDA uart
- I2S, SPI, I2C,
- Pcmcia/CF Card Interface
- SD/MMC Card Interface
- LC-Display Controller
- AC97 20 Bit Stereo Out, Low Noise design, programmable sampling rates, Headphone out
- AC97 Microphone input (mono) for electret capacitor microphones
- AC97 Line input stereo
- AC97 Touch Interface 4wire resistive
- AC97 4 x 10 Bit ADC
- Ethernet Controller 100/10 MBit 32 Bit Businterface
- USB 2.0 OTG 2 x Host, 2 x Slave or: 1xHost 1x OTG 1x Slave
- Parameter EEPROM
- 32 Bit Bus interface on SO-DIMM 200
- DDR-SODIMM-200 Connector, eg. AMP 1376408-1

**Board-Dimensions**

67.6 mm x 36.7 mm

**Current-Supply**

3.3 V DC, power consumption: t.b.d.

**Operating Systems**

Microsoft Windows CE.NET 4.2, 5.0